



ALTEST LAB ANALYSIS REPORT

Customer Name: SAMPLE

Part Number: 123456789

**ALTEST CORPORATION
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898 FAULSTICH COURT, SAN JOSE, CA. 95112**

Standard Specification; IDEA-STD-1010, AS6171, JESD31, ISO AS9001, AS9100D and AS6081 Certified.





ALTEST LAB ANALYSIS REPORT

WORK ORDER:		CUSTOMER:		DATE:	
MANUFACTURER:		PART NUMBER:		PO NUMBER:	
DATE CODE:		LOT / TRACE CODE:		QUANTITY:	
PART DESCRIPTION:				SALES REP:	

LAB PROCEDURES

<input type="checkbox"/>	IC PACKAGING VERIFICATION
<input type="checkbox"/>	3D X-RAY
<input type="checkbox"/>	DIMENSION MEASUREMENT
<input type="checkbox"/>	DIGITAL MICROSCOPE
<input type="checkbox"/>	FLYING PROBE TESTING
<input type="checkbox"/>	SAM (SCANNING ACOUSTIC MICROSCOPE)
<input type="checkbox"/>	CURVE TRACER TESTING
<input type="checkbox"/>	ELECTRICAL TESTING
<input type="checkbox"/>	COMPONENT DECAPPING
<input type="checkbox"/>	OXIDATION TESTING
<input type="checkbox"/>	AUTHENTICITY VERIFICATION

LAB RESULT	
FAILURE RATE	
NOTES	
SIGNATURE	

Scope

This is Altests Lab Analysis report, which delivers a comprehensive test report from non-destructive testing to destructive testing which allows isolation of the PCB/PCBA failure, including a root cause failure analysis to identify the conditions and specific phenomena behind the failure.

Disclaimer

Altest Lab Analysis service is dedicated to ensuring the highest standard of product testing in the industry and extend every effort to report reliable data and an accurate interpretation.

NO WARRANTY IS EXPRESSED OR IMPLIED REGARDING ANY WORK PERFORMED.



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PACKAGING AND LABELING		
PACKAGING 1	PACKAGING 2	ESD SEALED
PACKAGING 3	CUSTOMER INVOICE	MSL LABELING

YES	NO	N/A	PACKAGING & LABELING INFORMATION
			MATCHING MANUFACTURING PART NUMBER
			ORIGINAL PACKAGING INSPECTION
			DATE CODE INSPECTION
			LOT CODE INSPECTION
			MOISTURE SENSITIVE LABEL VISIBLE
			THIRD PARTY LABEL
			ROHS COMPLIANT
			QUANTITY MATCHING



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3D X-RAY REPORT	
Top	Bottom
Wire Bonding	Lead Frame
Result:	



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DIMENSION MEASUREMENT REPORT		
Body Length	Body Width	Overall Body & Leads
3D Body Thickness	3D Overall Thickness	Pitch
Lead Length	Lead Width	Component Datasheet



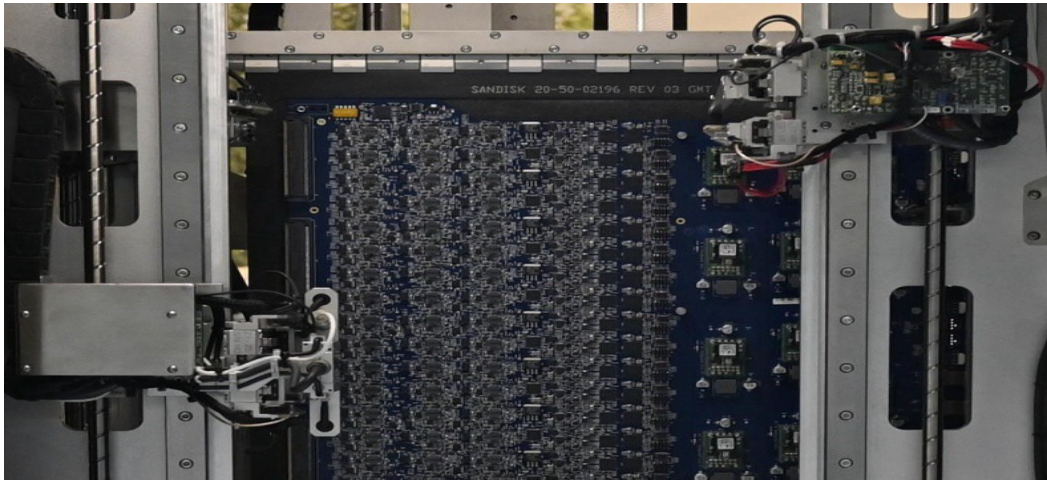
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DIGITAL MICROSCOPE REPORT				
Top	Bottom	Marking		
	<table border="1"><tr><td></td><td></td></tr></table>			
Leads	Top & Bottom Surface	Pin 1		
Leads	Side Leads	Oxidation on leads		



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FLYING PROBE TEST REPORT



Flying Probe Test is an electrical **PCBA** test.

Flying Probe Test includes:

- Standard analog in-circuit test
- Test ICs for open tracks on the PCBA
- Isolation Test to detect shorts on the PCBA
- Visual Tests for component presence, absence and rotation

Flying Probe Test requirements:

- CAD data: ODB++(Ver 7), IPC356, or Allegro ASCII file
- Schematic file for troubleshooting the PCBA

Board Qty:

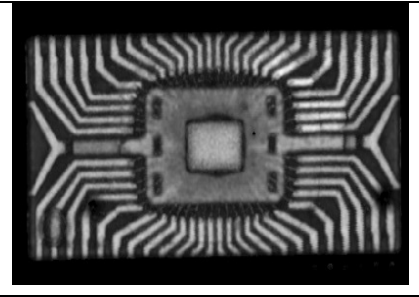
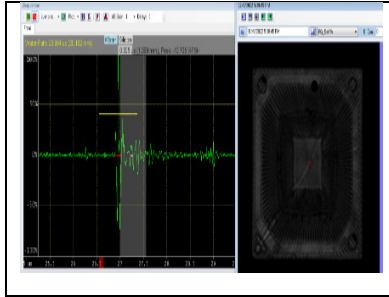
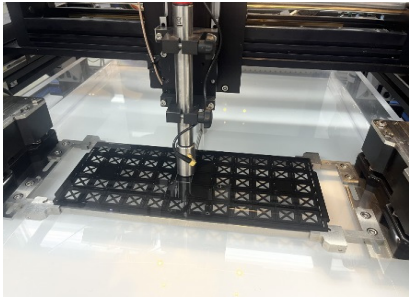
Pass Qty:

Attached Test Coverage Report: Yes No



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SCANNING ACOUSTIC MICROSCOPE REPORT



Semiconductor Package Failure Analysis:

- Counterfeit Detection
- Product Reliability
- Process Validation
- Vender Qualification
- Product Inspection
- Quality Control

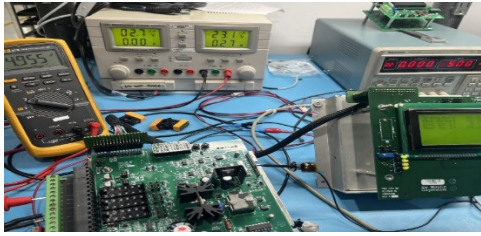
Top & Bottom Surface Scan	Top Layer Scan
Bottom Layer Scan	Scan Detection

Result:



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ELECTRICAL TEST



Types of Electrical Test Performed:

- Continuity Test
- AC/DC Voltage Test (Input and Output)
- Electrical Test to duplicate or reproduce the incorrect operation of the component.

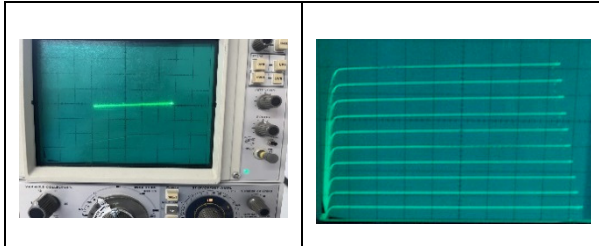
Test 1	Test 2
Test 3	Test 3

Result:



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CURVE TRACER



Curve Tracer: A High Voltage Testing for Semiconductor.

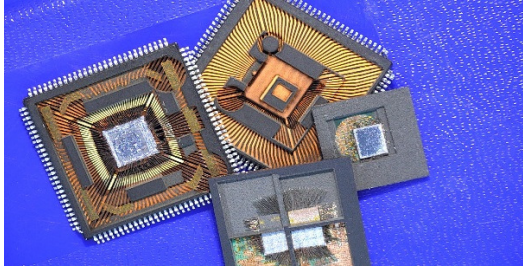
- Detect Electrically Damaged
- Detect Component Performance
- Verify Counterfeit Devices

Result:



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COMPONENT DECAPPING



Types of Decapsulation Performed:

- Open Surface Layer or Cavity for Further Analysis
- Validate Counterfeit Component
- Authentic Verification

Result: